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Electronic Version v1.1

Stylesheet Version v1.1.0

**Title of
Invention****INTEGRATED CIRCUIT DESIGN FOR SIGNAL INTEGRITY, AVOIDING
WELL PROXIMITY EFFECTS**

Application Number :

Date :

First Named Applicant: Karen A. Bard

Confirmation Number:

Attorney Docket Number: BUR920030172US1

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Submitted By:	Elec. Sign.	Sign. Capacity
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Documents being submitted:	Files
us-fee-sheet	BUR920030172US1-usfees.xml us-fee-sheet.xml us-fee-sheet.dtd
us-ids	BUR920030172US1-usidst.xml us-ids.dtd us-ids.xml
us-assignment	BUR920030172US1-usassn.xml us-assignment.xml us-assignment.dtd BUR920030172US1Assign1of3.tif BUR920030172US1Assign2of3.tif BUR920030172US1Assign3of3.tif
us-request	BUR920030172US1-usrequ.xml us-request.dtd us-request.xml BUR920030172US1Dec3of3.tif BUR920030172US1Dec1of3.tif BUR920030172US1Dec2of3.tif
us-declaration	BUR920030172US1spec-trans.xml
us-declaration	us-application-body.xml
us-declaration	application-body.dtd
application-body	wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd BUR920030172US1FIGSsheet1of3.tif BUR920030172US1FIGSsheet2of3.tif BUR920030172US1FIGSsheet3of3.tif

Comments

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